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gabriele saucier (editor of defect and fault - Gabriele Saucier is the author of Logic And Architecture Synthesis Fast Prototyping Of Vlsi: Defect and Fault Tolerance in VLSI Systems: Volume 2 by C H

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marco [defect and fault tolerance group] - Marco Ottavi is currently an Associate Professor at the University of Rome Tor Vergata , before he was a Research Professor with the same University as the

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